

of the changes made to the specification and to claim 81 by the current amendment. The attached page is captioned "Version With Markings To Show Changes Made."

### STATEMENT REQUESTING DELETION OF INVENTORS

The following inventors named in the parent application are not inventors of the invention claimed in the instant application:

Raymond J. Beffa  
Frank K. Ross  
Scott J. Derner  
Ronald L. Taylor  
John S. Mullin

Please file the instant application in the names of the remaining inventors (Keeth, Kinsman and Bunker) in accordance with 37 CFR 1.63 (d).

### CHANGE OF ADDRESS

Please note that the undersigned attorney's address has changed since the parent application was filed and that the undersigned attorney's address is correctly noted on form PTO/SB/05 filed herewith, and is correctly noted below.

Respectfully submitted



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Dated: 08 March 2001

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

|                      |                                      |   |                  |
|----------------------|--------------------------------------|---|------------------|
| <b>Applicant(s):</b> | Keeth, et al.                        | ) |                  |
|                      |                                      | ) | <b>Examiner:</b> |
| <b>Serial No.:</b>   | Not yet assigned                     | ) |                  |
|                      |                                      | ) | <b>Art Unit:</b> |
| <b>Filed:</b>        | March 8, 2001                        | ) |                  |
| <b>Entitled:</b>     | 256 MEG DYNAMIC RANDOM ACCESS MEMORY |   |                  |

**Version With Markings To Show Changes Made**

Page 52, delete the paragraph appearing at lines 17 – 21 and replace with the following:

Reference is hereby made to an appendix which contains [nine] eleven microfiche having a total of [fifty-two] sixty-six frames. The appendix contains 33 drawings on 44 frames which illustrate substantially the same information as is shown in FIGs. 1 – 113, but in a more connected format.

Claims 1-80 and 82 – 84 have been cancelled.

81. (Amended) A [dynamic random access] memory, comprising:

a plurality of memory cells:

a plurality of pads;

a plurality of peripheral devices for transferring data between said memory cells and said plurality of pads;

a plurality of voltage supplies for generating a plurality of supply voltages;

a power distribution bus for delivering said supply voltages; and

a package encapsulating said memory, said package including a lead frame forming [apart] a part of said power distribution bus.

New claims 85 - 159 have been added.